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F-6810

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kenji SHIGEKI, et al.

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Serial No. : 09/772,027

FEB 2 6 2004

Filed : January 29, 2001

Technology Center 2100

For : LOGIC INTEGRATED CIRCUIT, AND
RECORDING MEDIUM READABLE BY A
COMPUTER, WHICH STORES A SOURCE OF
CPU CORE ON SAID LOGIC INTEGRATED
CIRCUIT

Group Art Unit : UNKNOWN

Examiner : UNKNOWN

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INFORMATION DISCLOSURE STATEMENT

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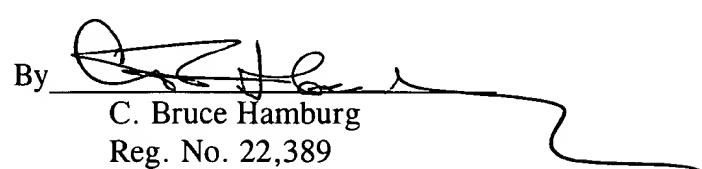
Submitted herewith is an Information Disclosure Citation together with copies of two of the documents referred to therein. The degree of relevance of the documents referred to in the Information Disclosure Citation is set forth in the European Search Report also submitted herewith.

I hereby certify that each item of information contained in the Information Disclosure Citation was first cited in any communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

Respectfully submitted,

Jordan and Hamburg LLP

By

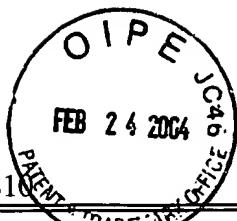


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F-6810

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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)					Applicant: Kenji SHIGEKI, et al.			
					Filing Date: January 29, 2001		Group:	
U.S. PATENT DOCUMENTS								
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
<p>TRANSLATION KEY: * English Abstract. ^F Concise statement of relevance provided in foreign search report. ^C Concise statement of relevance provided in specification. ^S Concise statement of relevance provided in IDS. ^P Relevant portion of reference translated. ^O English abstract only - copy of reference in pct search.</p>								
OTHER INFORMATION DISCLOSURE CITATIONS (Including Author, Title, Date, Pertinent Pages, Etc.)								
		1/1/1989 VLSI RISC architecture and organisation" Furber, S.B. Marcel Dekker Inc., New York, US, XP002266554 pages 238-239						
		5/10/1988 Optimized for Performance and Cost/Performance: The AM29000 Family Chu, Paul Electro, Electronic Conventions Management, Los Angeles, US, XP000118801 Vol. 13 pages 42-4, 1-7						
EXAMINER				DATE CONSIDERED				
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